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Date 28. 08. 03

World Intellectual Property Organization
PCT Administration Division
34 Chemin des Colombettes
1211 GENEVA 20
SWITZERLAND

"Amendment of the claims under Article 19 (1) (Rule 46) "

Re: International Application No. PCT/JP03/03193
Applicant: MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.
Agent OKADA KAZUhide
International Filing Date: 17. 03. 03

Dear Sir.

The applicant, who received the International Search Report relating to the above identified International Application transmitted on 01.07.03, hereby files amendment under Article 19(1) as in the attached sheets.

Thus claims 5,14 are canceled and claims 1,2,3,4,6,7,8,9,10,11,12,13,15,16,17,18, 19,20,21 are retained unchanged.

Very truly yours

Kazuhide Okada
OKADA KAZUhide

Attachment

(1)Amendment under Article 19 (1)

(Replacement sheets of pages 48and52 of the claimes)

2sheet s

REPLACED BY
APT/9/AMDT

5. An image conversion system comprising:

an input device to which a compressed image signal in an interlace system having a format of 60 fields/second, which has been combined and compressed on a frame basis, is input; and

5 a frame-data extracting control device which selectively extracts from the input device the first, second, fourth and fifth frame data among the first to fifth frame data that form the compressed image signal and are consecutively arranged with each other.

10 6. An image-editing device comprising:

an input device to which a compressed image signal in an interlace system having a format of 60 fields/second, which has been combined and compressed on a frame basis, is input;

15 a frame-data extracting control device which selectively extracts from the input device the first, second, fourth and fifth frame data among the first to fifth frame data that form the compressed image signal and are consecutively arranged with each other;

20 a recording/reproducing device which records/reproduces the compressed image signal extracted by the frame-data extracting control device;

an image expanding device which expands the compressed image signal reproduced by the recording/reproducing device;

25 an image output device which displays the expanded image signal; and

an editing device which edits the compressed image signal reproduced from the recording/reproducing device on a frame basis.

7. An imaging system comprising:

30 an imaging device which picks up a first image signal in an image format of 24 frames/second;

a temporary recording device which temporarily records the first image signal picked up by the imaging device; and

corresponding a time-code value $4n + 1$ of the first image signal to field data located at odd fields of a frame corresponding a time-code value $5n + 2$ of the second image signal;

5 converting field data located at even fields of a frame corresponding a time-code value $4n + 2$ of the first image signal to field data located at even fields of a frame corresponding a time-code value $5n + 2$ of the second image signal;

10 converting field data located at odd fields of a frame corresponding a time-code value $4n + 2$ of the first image signal to field data located at odd fields of a frame corresponding a time-code value $5n + 3$ of the second image signal;

15 converting field data located at even fields of a frame corresponding a time-code value $4n + 2$ of the first image signal to field data located at even fields of a frame corresponding a time-code value $5n + 3$ of the second image signal;

converting field data located at odd fields of a frame corresponding a time-code value $4n + 3$ of the first image signal to field data located at odd fields of a frame corresponding a time-code value $5n + 4$ of the second image signal; and

20 converting field data located at even fields of a frame corresponding a time-code value $4n + 3$ of the first image signal to field data located at even fields of a frame corresponding a time-code value $5n + 4$ of the second image signal (where n is an integer of 0 to 5).

25 14. An image conversion system comprising:

an input device to which a compressed image signal in an interlace system having a format of 60 fields/second, which has been combined and compressed on a frame basis, is input; and

30 a frame-data extracting control device which extracts pieces of frame data located at frame positions corresponding to time-code values of $5n$, $5n + 1$, $5n + 3$, $5n + 4$ (n : an integer of 0 to 5) from the input device.

15. An image-editing device comprising: